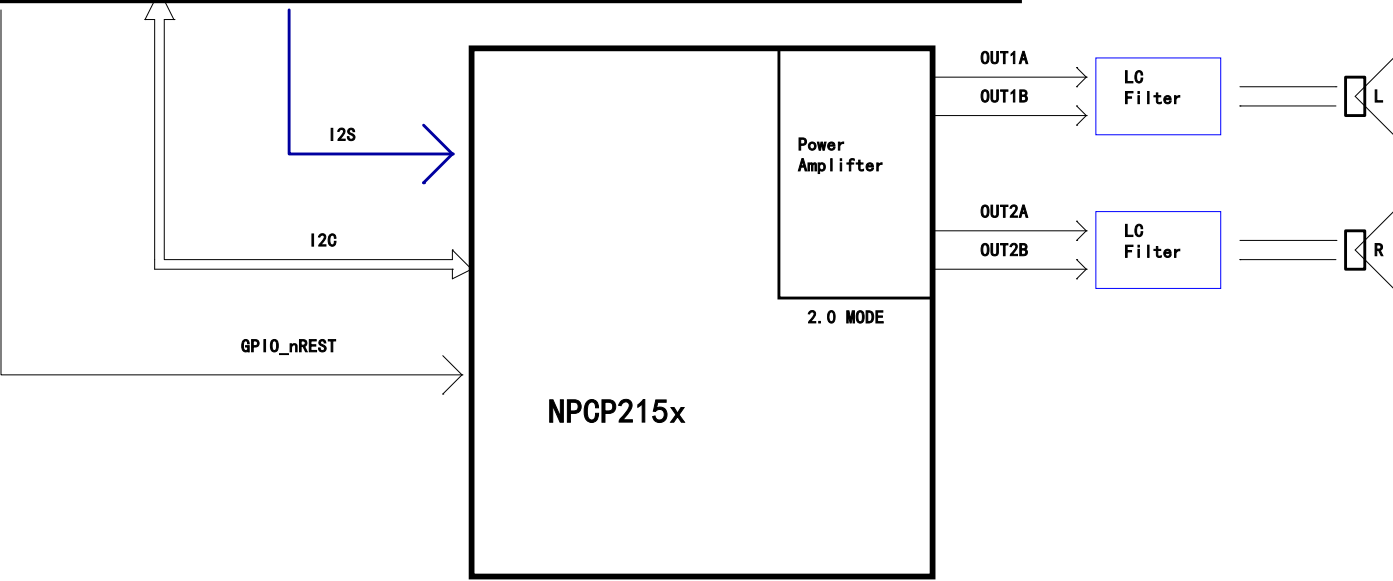
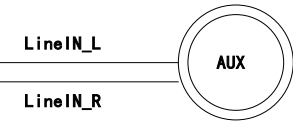
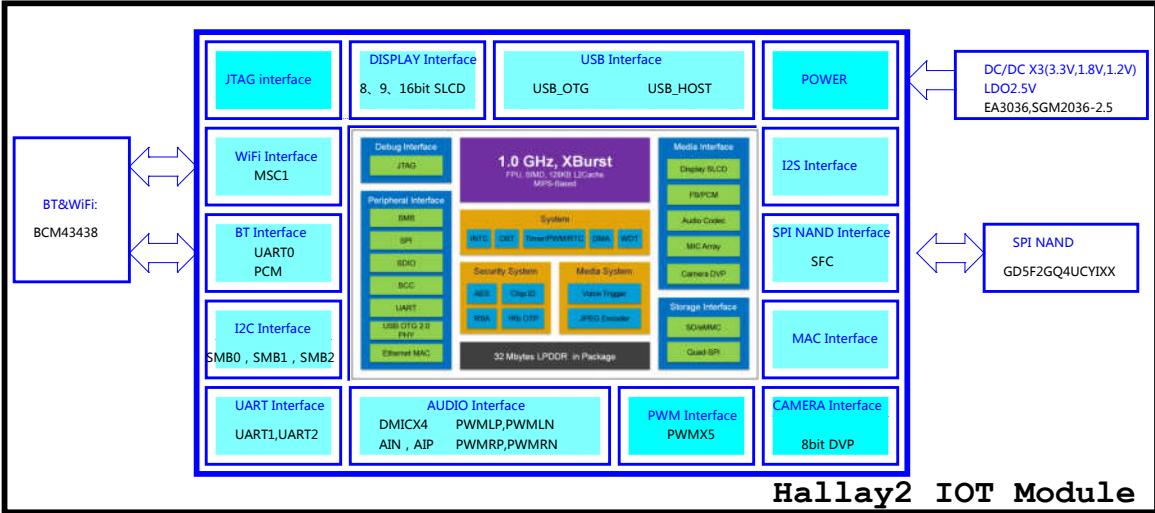
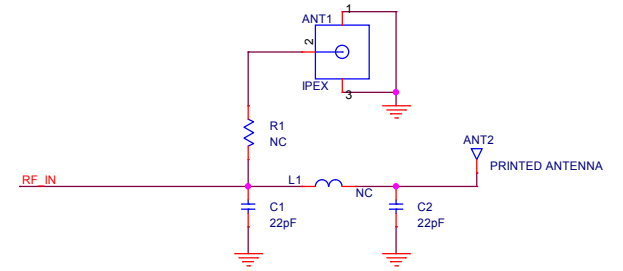
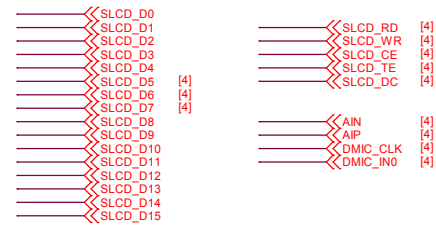
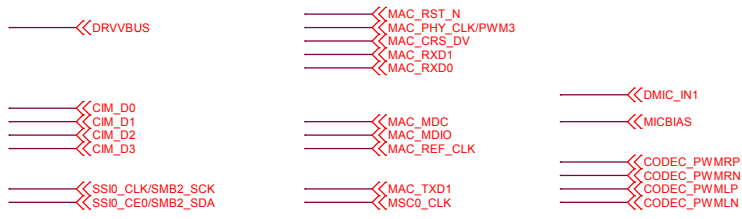


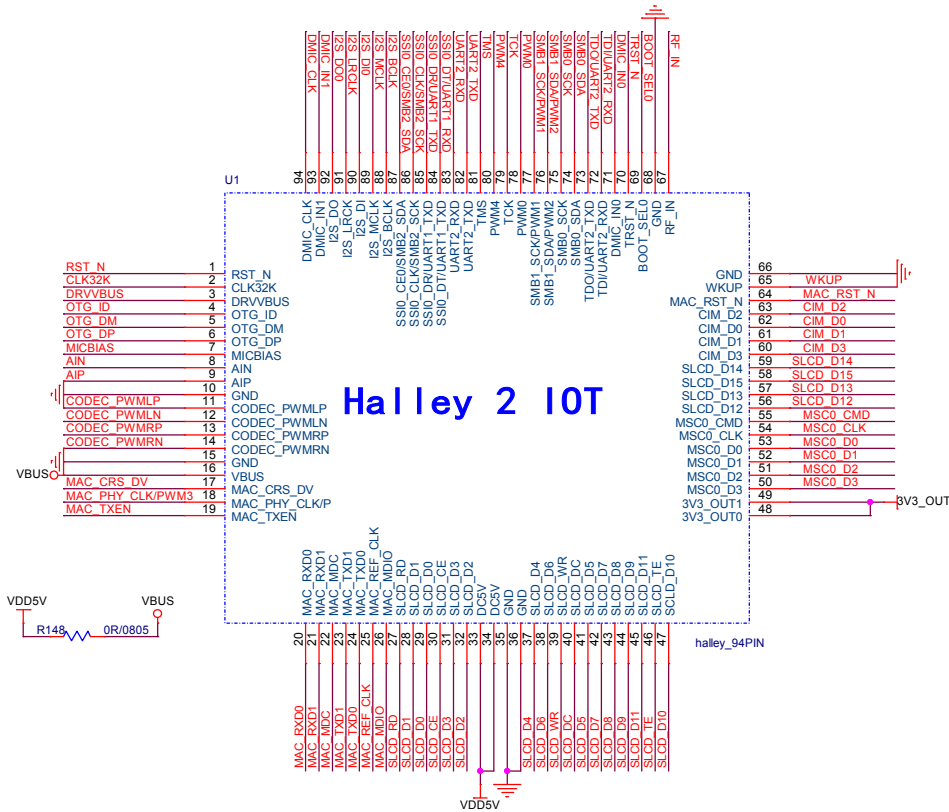
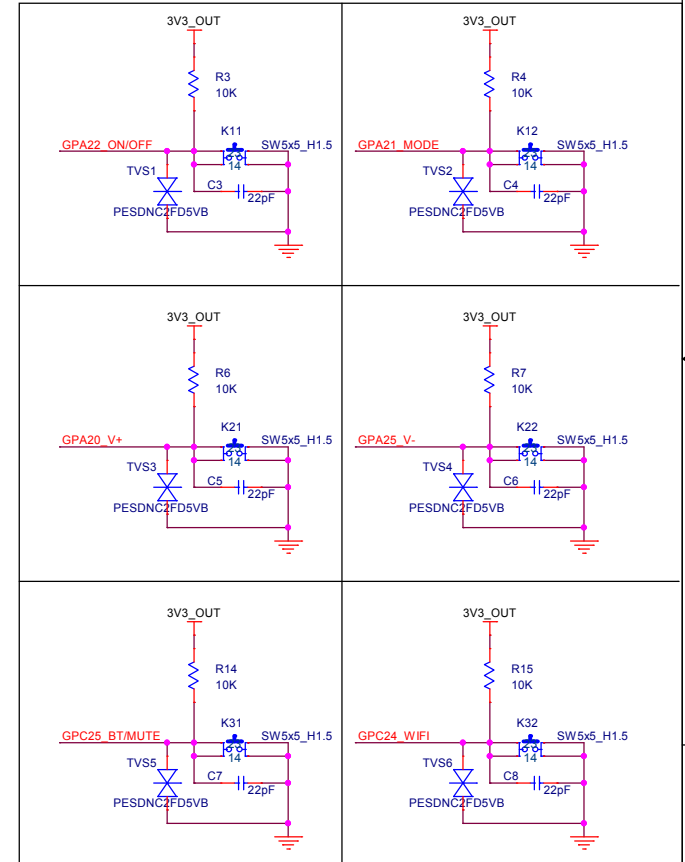
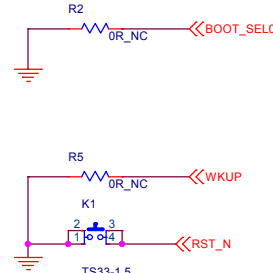
TFT 2.4' LCD  
(16 bit)

SLCD

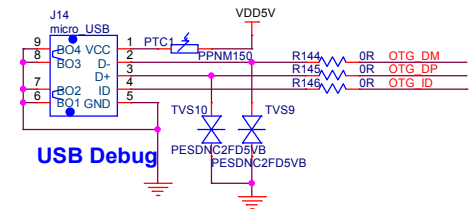




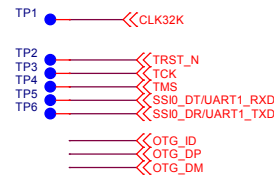
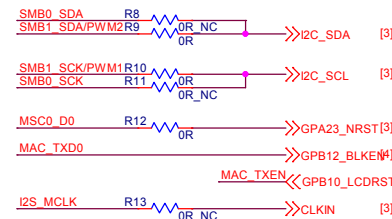
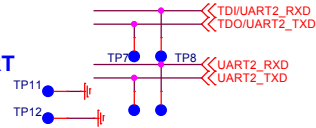
**BOOT MODE:**  
**HIGH (default) - FSC Boot@24MHz**  
**LOW - USB Boot@24MHz**

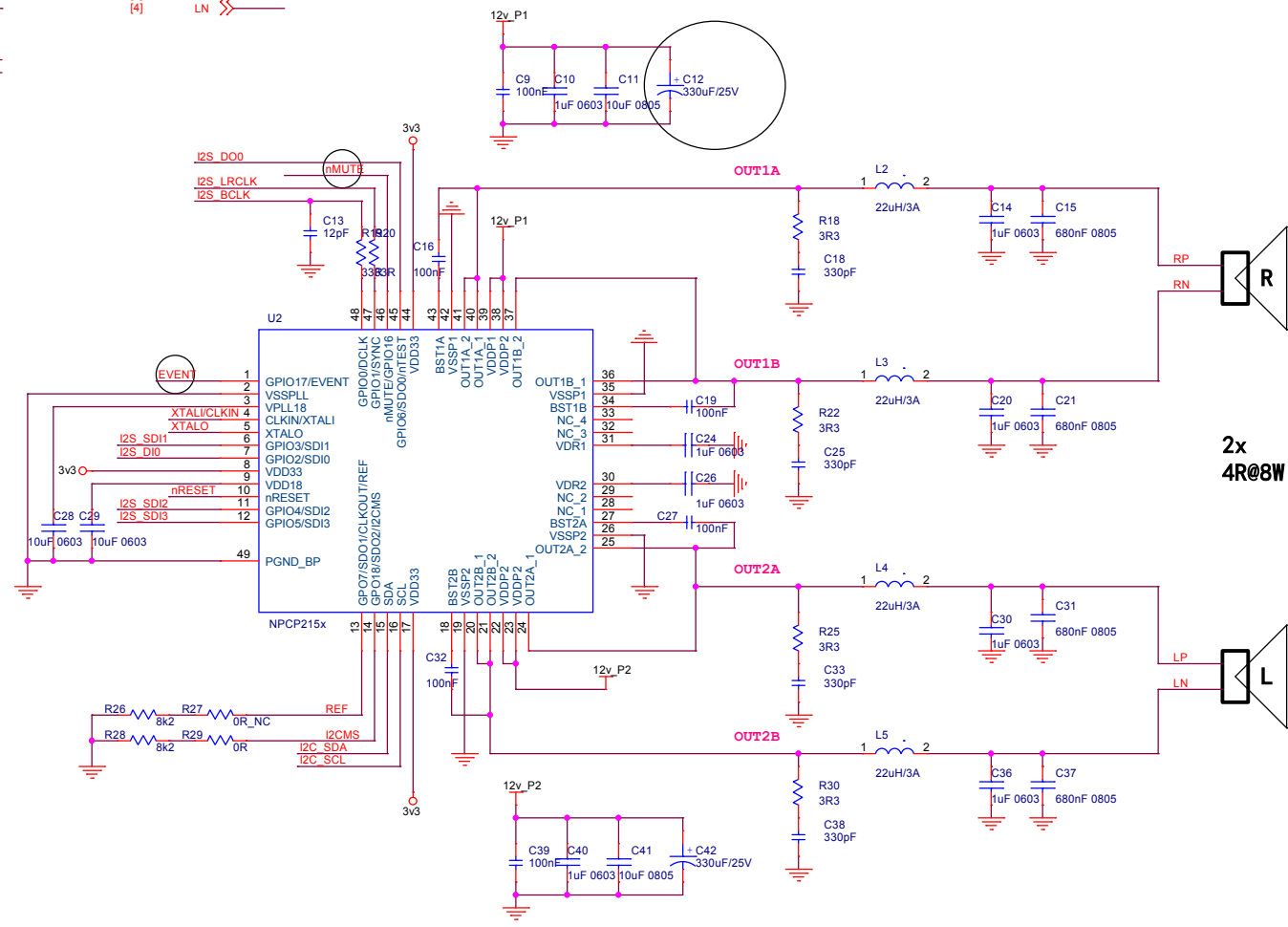
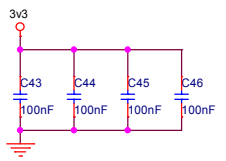
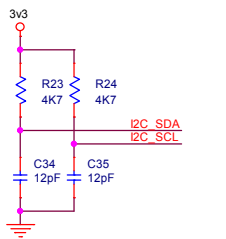
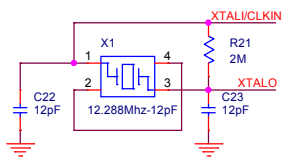
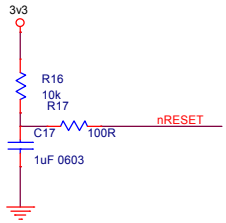


# Halley 2 IOT

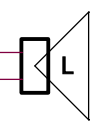
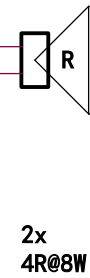


## Debug UART

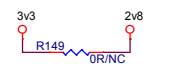
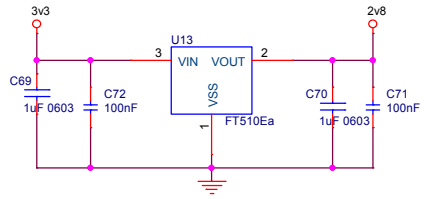
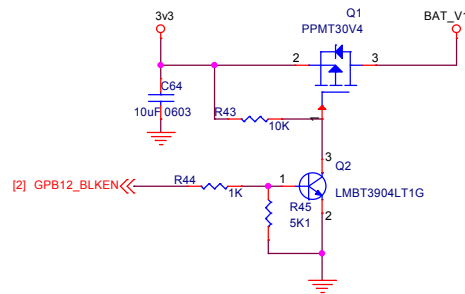
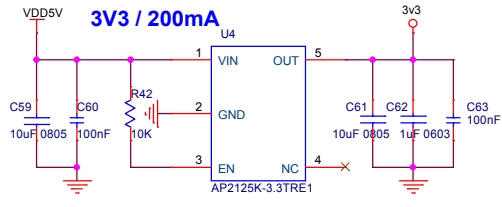
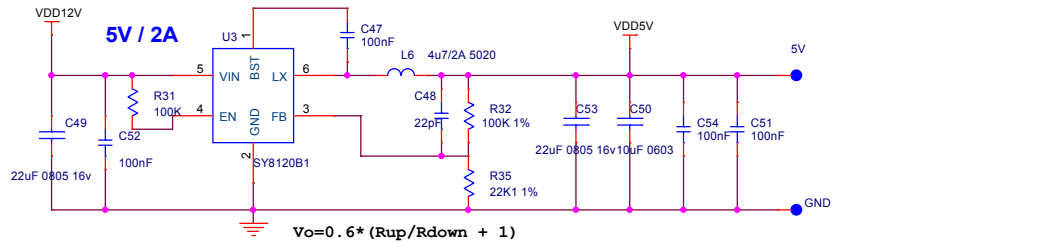




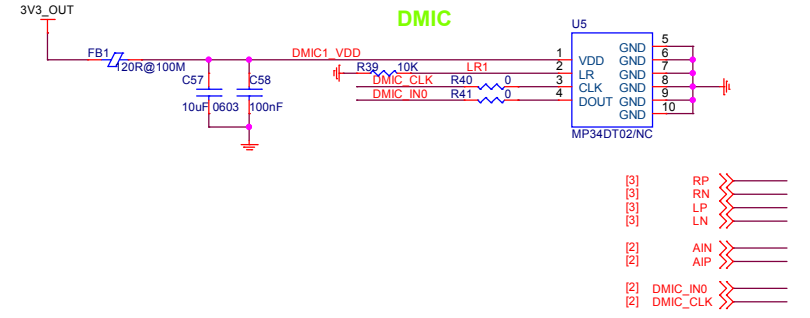
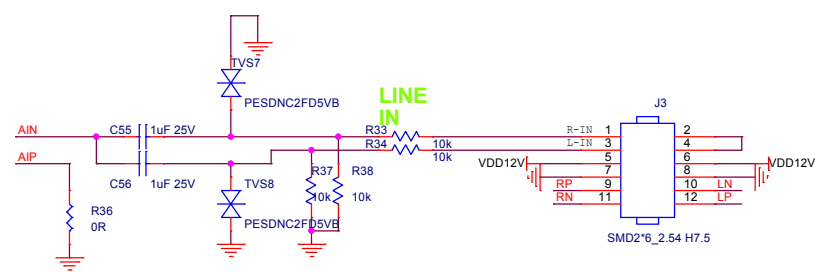
I2CMS:  
 HIGH (Internal PU) - I2C Slave  
 LOW (External PU) - I2C Master  
 REF: Clock Reference Strap;  
 HIGH (Internal PU) - DCLK  
 LOW (External PU) - CLKIN



2x  
4R08W

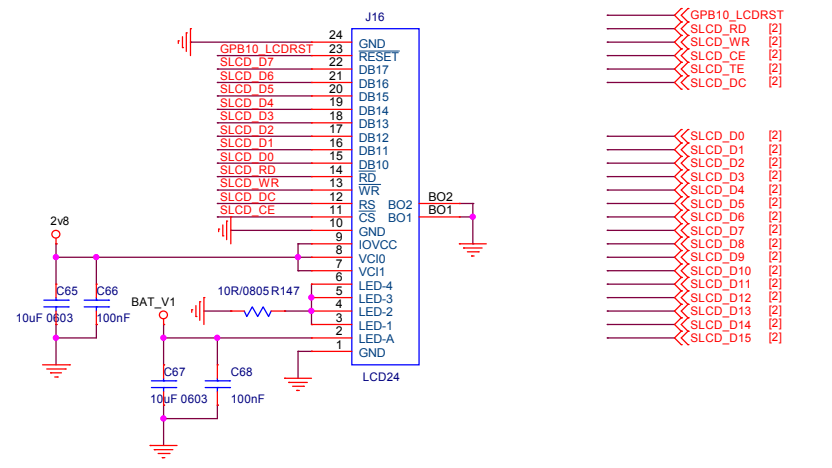


- |                         |                    |                    |                    |                    |
|-------------------------|--------------------|--------------------|--------------------|--------------------|
| MK1<br>mark1_0e0_5<br>⊗ | H7<br>hole20u<br>⊗ | H5<br>hole20u<br>⊗ | H3<br>hole20u<br>⊗ | H1<br>hole20u<br>⊗ |
| MK2<br>mark1_0e0_5<br>⊗ | H8<br>hole20u<br>⊗ | H6<br>hole20u<br>⊗ | H4<br>hole20u<br>⊗ | H2<br>hole20u<br>⊗ |
| MK3<br>mark1_0e0_5<br>⊗ |                    |                    |                    |                    |
| MK4<br>mark1_0e0_5<br>⊗ |                    |                    |                    |                    |



- [3] RP
- [3] RN
- [3] LP
- [3] LN
- [2] AIN
- [2] AIP
- [2] DMIC\_IN0
- [2] DMIC\_CLK

**LCD CONNECTER**



- GPB10\_LCDRST
- SLCD\_RD [2]
- SLCD\_WR [2]
- SLCD\_CE [2]
- SLCD\_TE [2]
- SLCD\_DC [2]
- SLCD\_D0 [2]
- SLCD\_D1 [2]
- SLCD\_D2 [2]
- SLCD\_D3 [2]
- SLCD\_D4 [2]
- SLCD\_D5 [2]
- SLCD\_D6 [2]
- SLCD\_D7 [2]
- SLCD\_D8 [2]
- SLCD\_D9 [2]
- SLCD\_D10 [2]
- SLCD\_D11 [2]
- SLCD\_D12 [2]
- SLCD\_D13 [2]
- SLCD\_D14 [2]
- SLCD\_D15 [2]

屏为8bit